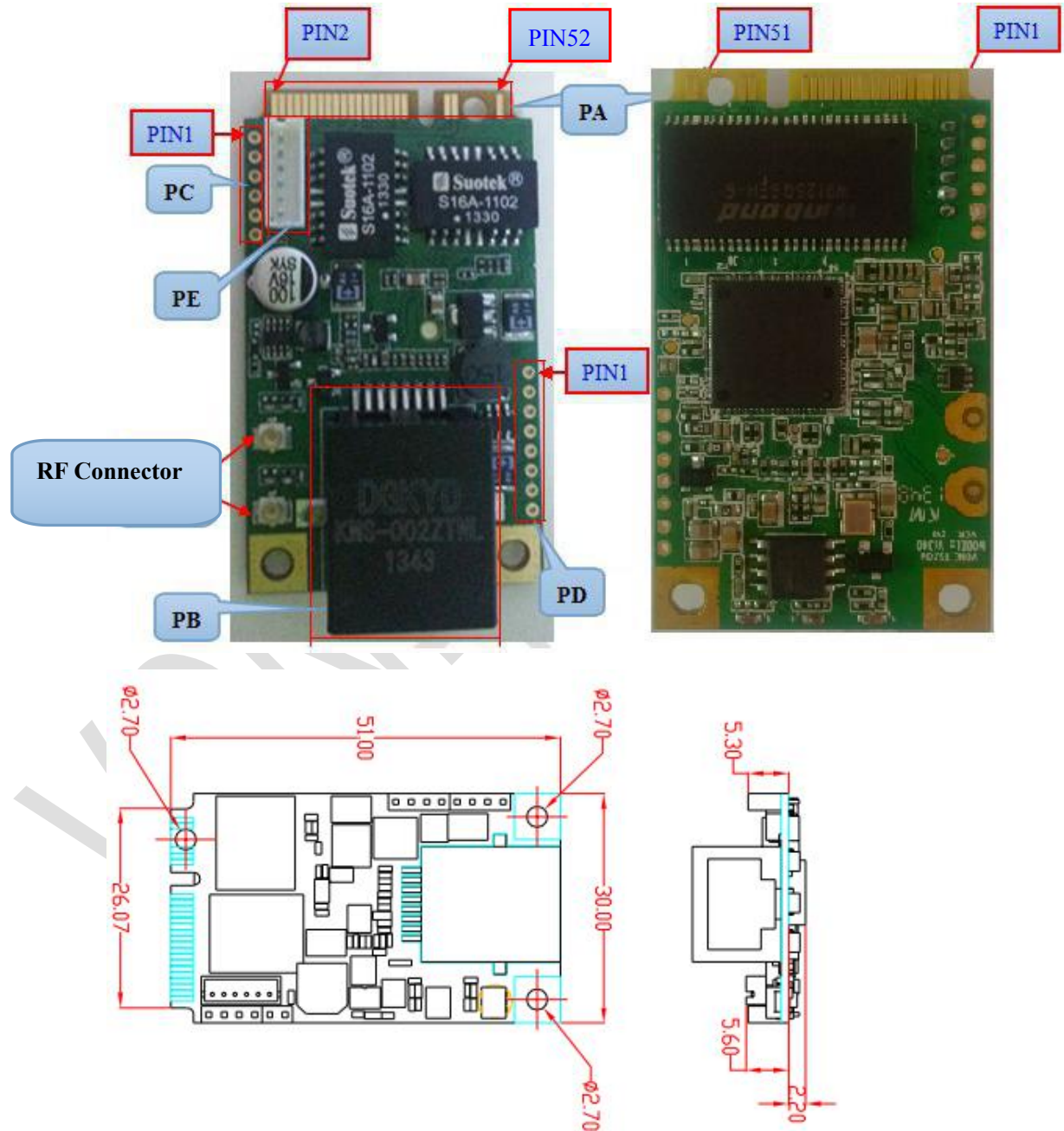


VM300_3V0 WiFi Module Specification

1. Features

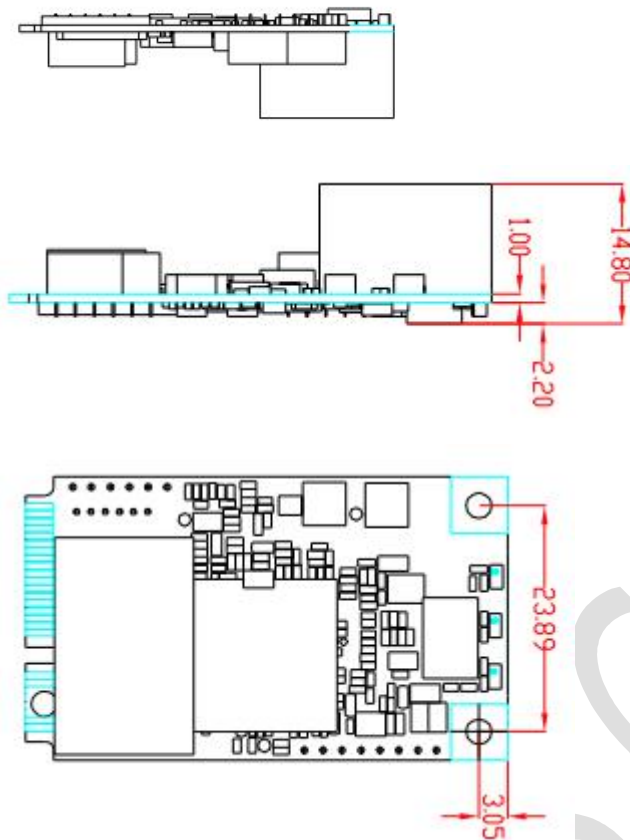
1.1 Hardware Features

- Module Outline:



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- Wide operating voltage: DC5V--15V
- Main chip : MT7620N、 580MHz.
- 32Mb SDRAM,can be extended 64Mb; 8Mb SPI Flash, can be extended 16Mb(Byte).
- WiFi working frequency:2.4GHz ,2T2R dual antenna ,300Mbps transmission rate.
- Support 1-14WiFichannels.
- Rated Power< 2W.
- Output power of Module RF :15dbm--16.5dbm.
- Adopt the original creation D/A temperature compensated auto frequency control technology (TAFC) ,make sure the lasting stability to ensure that the WiFi signal is not dropped.
- Support 802.11b/g/n mode.
- Operating ambient temperature : -25°C ---- 55°C

1.2 Features

- Two software-controllable operating modes: routing mode, Bridge+Repeater mode.
- Support intelligent transparent bridge mode, also support AP Client and AP Station.
- Built-in WEB configuration services, available configure module parameters through the web.
- Built-in SOCKET monitoring services, used to control two GPIO (1.5A) output, nine kinds of GPIO control state to facilitate the secondary development and function expansion.
- Support VDNS technology, in bridge mode, can be configured via logging on the domain login page.
- Two external antenna RF Female.
- Support for online firmware upgrade.
- The main function the interface :

Female-interface / Sub-interface	MINI PCIE (PA)	6PIN Female (PE)	6PIN Pin Interface (PC)	8PIN Pin Interface (PD)	RJ45 Female (PB)	Directions
VIN+	√	√	√			DC5V--15V
WAN	√				√	Software-controlled interchangeable
LAN	√	√	√			
WAN Status Indication	√			√		1.MOS transistor open collector output, built-in 330Ω current limiting resistor 2.Output Current: 10mA
WiFi Bridge connection status indication	√			√		1.MOS transistor open collector output, built-in 330Ω current limiting resistor 2.Output Current:10mA
Reset signal input	√			√		Restore factory parameter

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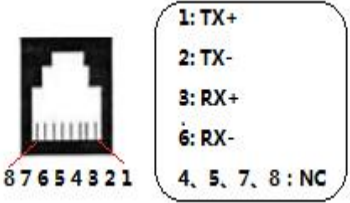
GPIO_01 output	√			√		1. MOS transistor open collector output 2.Limit voltage: 15V 3.Output Current: 1.5A 4.Software control 9 kinds of controllable state
GPIO_02 output	√			√		
UART2	√					UART Port for Debug

2. Hardware interface details

Female-interface Sub-interface	Interface pin definitions(PIN)				Directions	
	PA	PC, PE	PB	PD		
VIN+	2, 24, 39	6			DC5V--15V	
GND	4, 9, 15, 18, 21, 26, 27, 34, 35	5		8	Module Place	
WAN	13: TX+ 16: TX- 17: RX+ 19: RX-		Standard RJ45 Ethernet port female		PB' s WAN pin parallel with the PA' s WAN	Configure through the Web, WAN or LAN can be exchanged LAN
LAN	11: RX- 7: RX+ 5: TX- 3: TX+	1: RX- 2: RX+ 3: TX- 4: TX+			PC and PE' s LAN pin parallel to PA' s LAN	
WAN Status Indication	25			2	1.MOS transistor open collector output, built-in 330Ω current limiting resistor 2.Output Current: 10mA	
AP Client connection Status Indication	23			1	1.MOS transistor open collector output, built-in 330Ω current limiting resistor 2.Output Current:10mA	

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Reset signal input	32			5	After Module work properly, remain low at least 3 seconds, the module will restore the factory parameters
GPIO_01 output	28			3	1.MOS transistor open collector output
GPIO_02 output	30			4	2.Limit voltage : 15V 3.Output Current : 1.5A 4.Software Control, 9 kinds of controllable status
UART2	36:TX 38:RX				TX/RX of UART2 for Debug
Standard RJ45 female pin definitions reference					

3. Supplied accessories

- 2db external antenna with RF cable ties



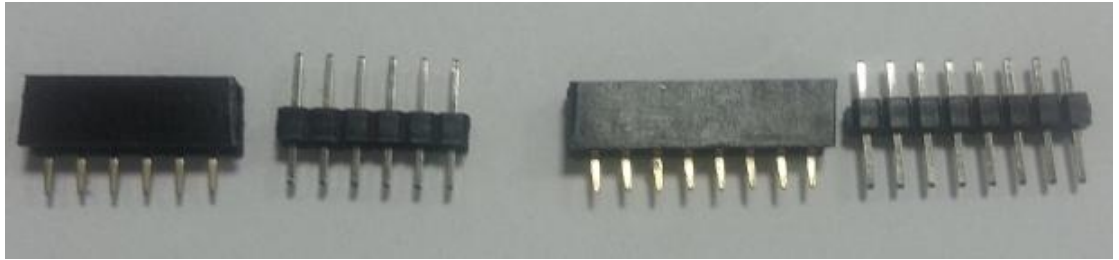
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- 6PIN and 8PIN pin male and female



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